### **Combining Optimistic and Pessimistic DVS Scheduling: An Adaptive Scheme and Analysis**

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- Motivation
- Background
- Example
- Proposed Adaptive DVS Scheme
- Design and Verification of Adaptive Scheme
- Experimental Evaluation
- Conclusions

## **Embedded Systems**

#### **Computer systems physically embedded into larger device**



#### Requirements

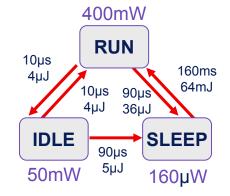
- Performance
- Energy
- Fault tolerance
- Size
- Weight
- Cost

## **Methods for CPU Power Management**

### • Dynamic Voltage Scaling (DVS)

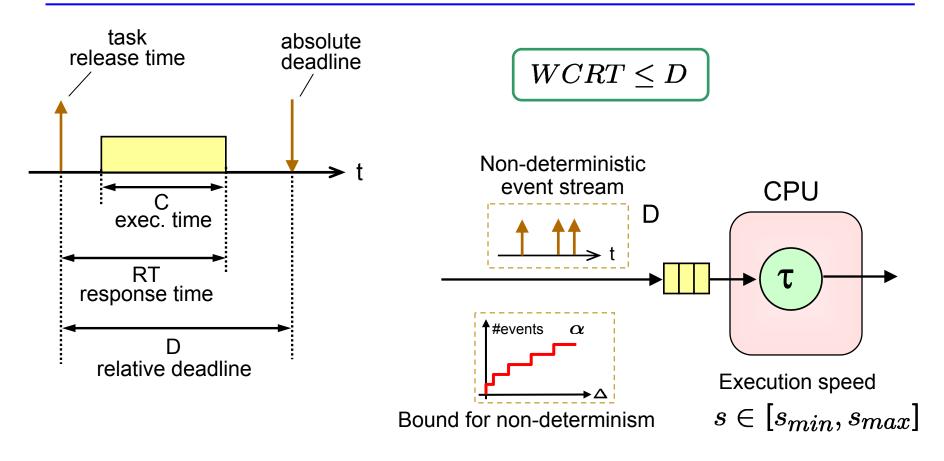
- For CMOS circuits  $E \propto V_{dd}^2$  (ignoring leakage)
- Save energy by reducing supply voltage (clock frequency)

- Dynamic Power Management (DPM)
  - Switch between different power states
  - Helps to reduce power dissipation due to leakage



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# **System Model**



Goal: Minimize energy consumption while guaranteeing deadlines

# **Related Work**

#### • Offline DVS Approaches

- Take scheduling decisions statically (at design time) based on expected worst-case workload
- E.g. [Yao et al. 1995], [Quan et al. 2007], [Maxiaguine et al. 2005]
- Problem: If the actual event trace differs from the worst-case, the execution speed is unnecessarily high!

#### $\rightarrow$ They are often too pessimistic (waste energy)

#### • Online DVS Approaches

- Take scheduling decisions dynamically (at run time) by adapting to actual workload
- E.g. [Yao et al. 1995], [Aydin et al. 2001], [Bansal et al. 2005]
- Problem: They may go above the maximum available speed!

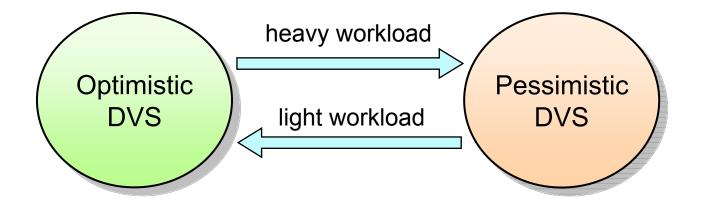
### $\rightarrow$ They may be too optimistic (improvident)

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#### Adaptive scheme which combines Online and Offline DVS

Idea: Apply optimistic online DVS if system is light-loaded and pessimistic offline DVS if system is heavy-loaded

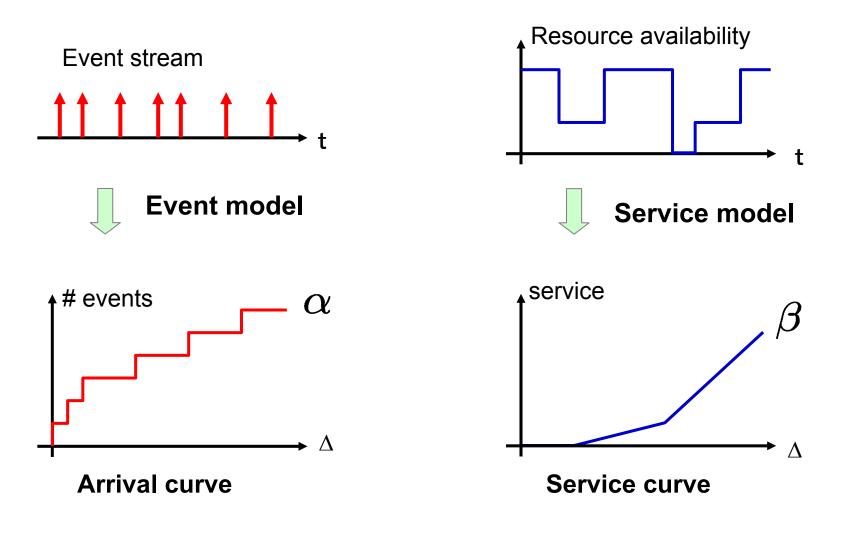
Key issue: Decide when to switch between the two modes



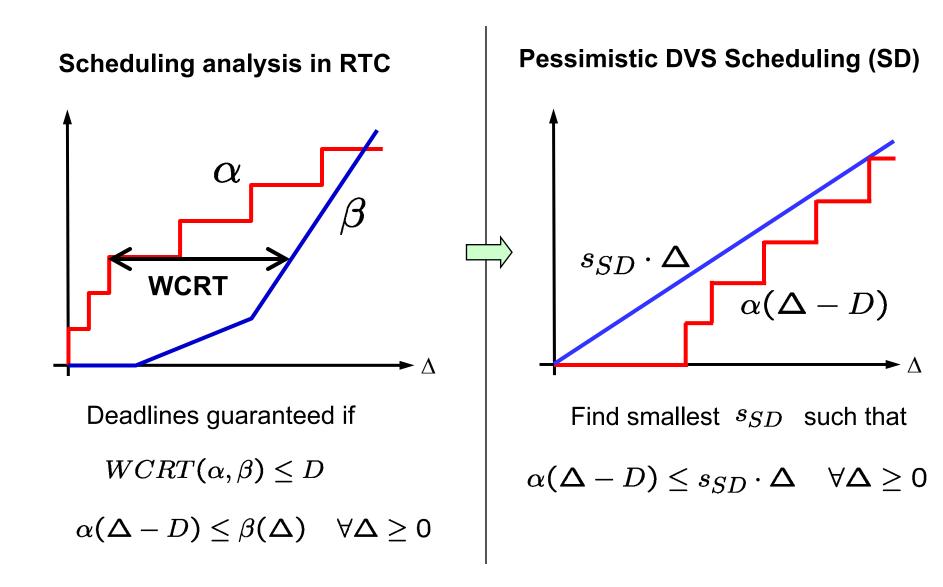
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## **Event and Resource Models**

Real-Time Calculus (RTC) [Thiele et al. 2000]



## **Pessimistic Offline DVS Scheduling**



# **Optimistic Online DVS Scheduling**

**OPT Algorithm** [Yao Demers Shenker 1995]

- Greedily select minimum speed that guarantees all deadlines considering only arrived events
- Event-driven algorithm: Speed changes only at event arrival or completion

$$s(t) = \max \left\{ s'_{\min}, \max_{e_j} \left\{ \sum_{e_i:a_i \leq t, e_i \leq e_j} \frac{C_i(t)}{d_j - t} \right\} \right\}$$

$$c \dots \text{ remaining execution time}$$

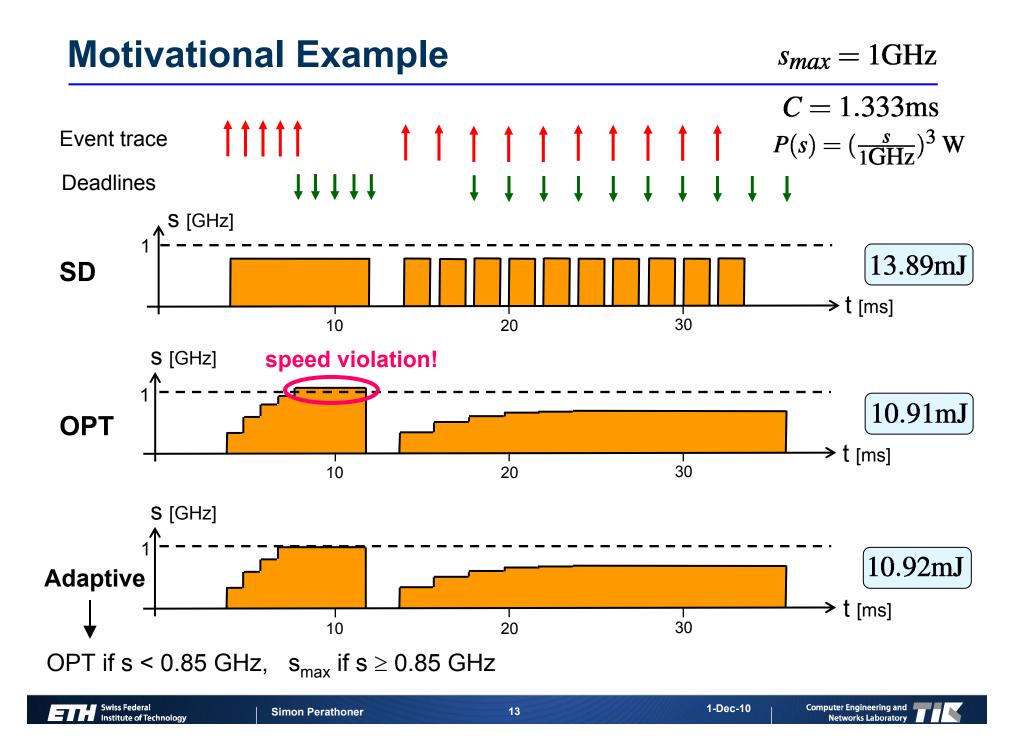
$$a \dots \text{ arrival time}$$

$$d \dots \text{ deadline (abs.)}$$

$$\leq \dots \text{ priority order}$$

$$Analytical bound for max. speed: [Chen et al. 2009]$$

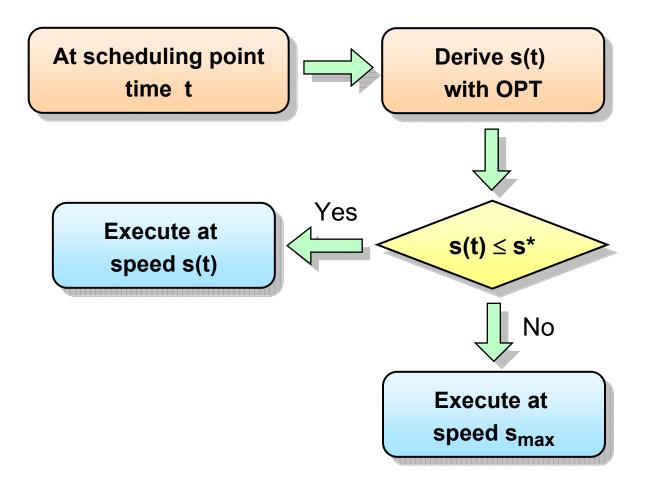
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## **Adaptive DVS Scheme**

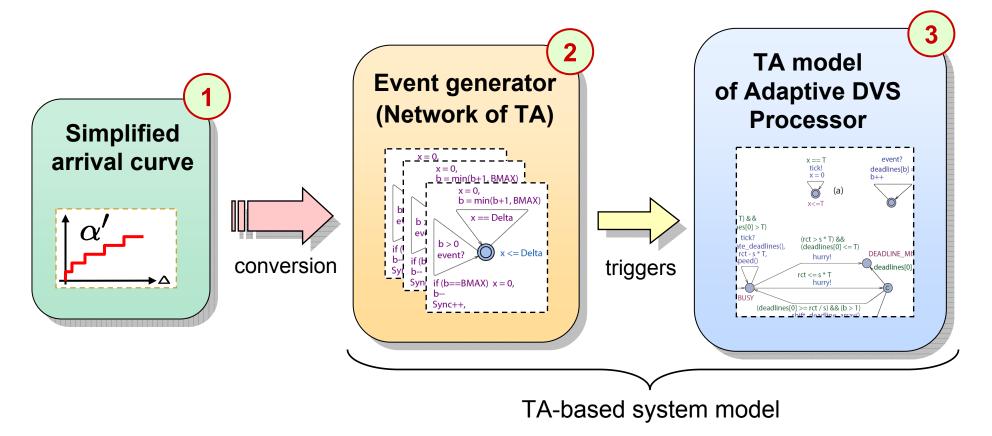
s\* = Threshold speed



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## **Design and Verification - Overview**

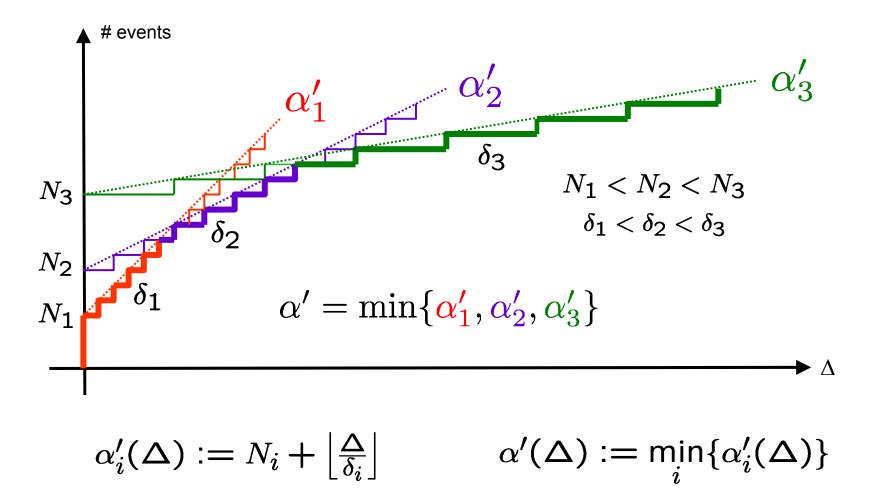
Approach based on hybrid analysis method of [Lampka et al. 2009]



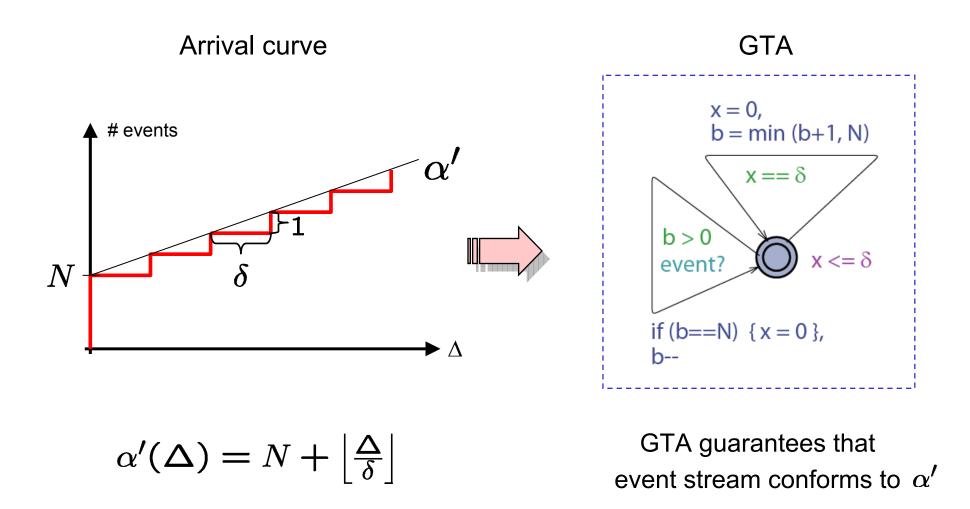
Use model checker UPPAAL and binary search to determine max. s\*

# **1** Simplified Arrival Curves

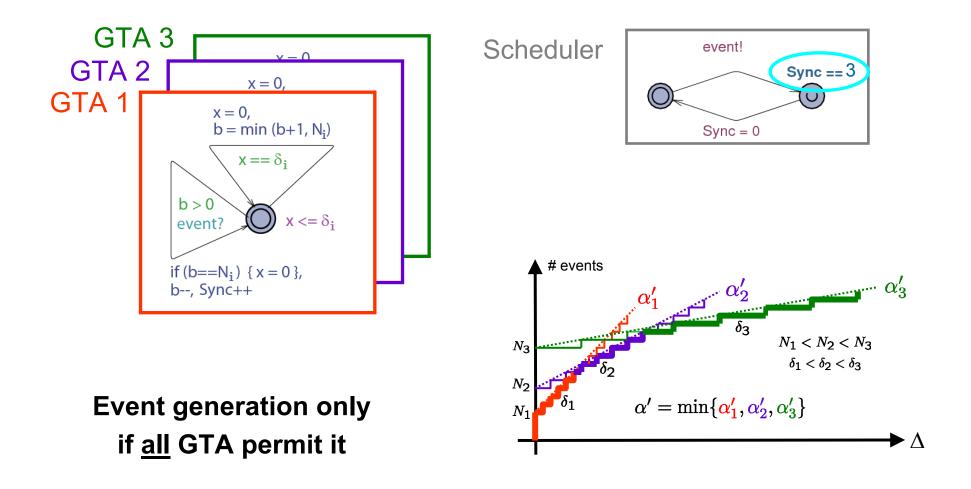
Pseudo-concave arrival curve (increasing step widths)



# **2** TA Representation of Arrival Curve



# **2** TA Representation of Arrival Curve



# **3** TA Model of Adaptive DVS Processor

Speed computation in <u>event-driven</u> OPT requires knowledge of remaining execution time and time left to deadline

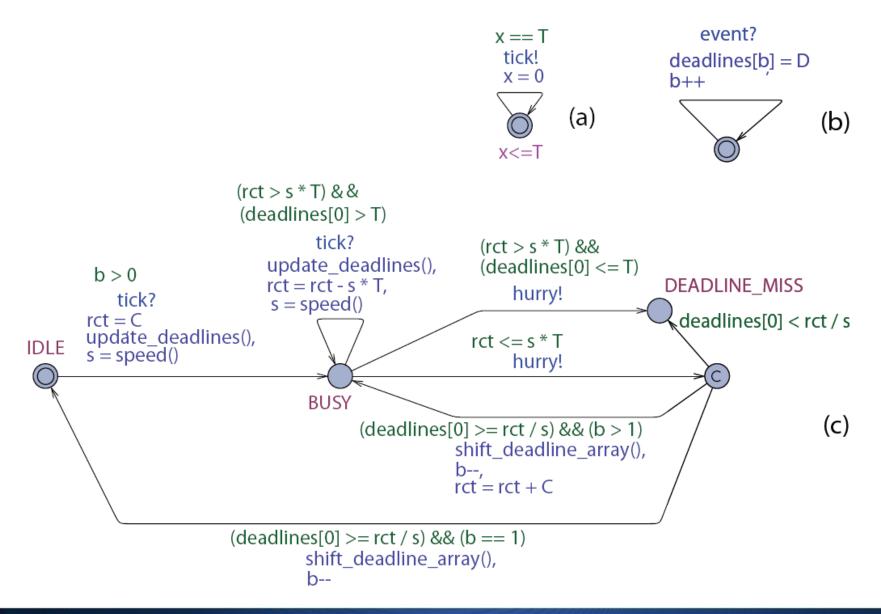
⇒ Exact modeling of OPT is not possible in UPPAAL (because computations on clock variables are not supported)

Finding a conservative TA approximation of event-driven OPT is not trivial!

We devise a formal model for a time-driven variant of OPT

- Based on discrete time (clock ticks with period T )
- Adaptation of event release times:  $a' := \lceil \frac{a}{T} \rceil T$
- Adaptation of deadlines:  $d' := \lfloor \frac{d}{T} \rfloor T$

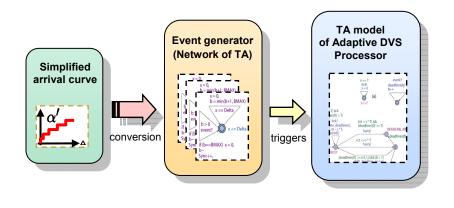
# **3** TA Model of Adaptive DVS Processor



## No additional run-time overhead

### **Design time**

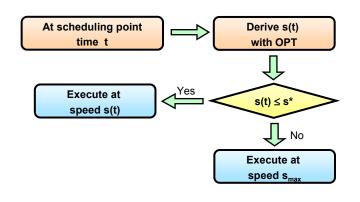
- Parameterization and validation of adaptive DVS scheduler
- Application of (expensive) state-based formal verification



determine threshold speed s\*

### **Run time**

• Simple variant of OPT algorithm



use threshold speed s\*

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## **Experimental Setup**

Comparison of static DVS (SD), online DVS (OPT), and adaptive DVS (AD)

Set of 6 periodic event streams with large non-deterministic jitters

Parameters [ms]:

	Ι	II	III	IV	V	VI
р	198	102	283	239	148	114
J	387	70	269	222	91	13
d	48	45	58	65	78	0
C	30	35	77	69	53	52
D	110	140	310	280	200	120

Considered processor: Intel XScale  $s_{max} = 0.5 \text{GHz}$  $P(s) = 0.04 + + \hbar (1.56(\frac{s}{0.5 \text{GHz}})^3) \text{ W}$ 

s\* for AD is computed with UPPAAL model checker

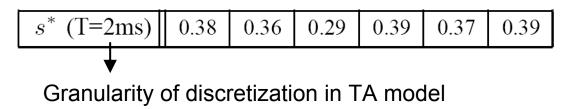
## Results

### Maximum speeds for SD and OPT (analytical bounds) [GHz]:

	Ι	II	III	IV	V	VI
$s_{SD}$	0.44	0.38	0.42	0.4	0.39	0.47
$s_{OPT}^{max}$	0.513	0.505	0.501	0.506	0.506	0.506

OPT violates *smax* 

### Maximal threshold speeds s\* for AD [GHz]:



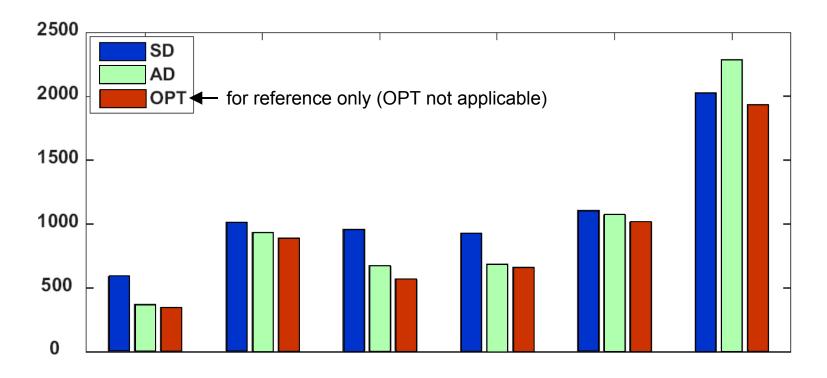
#### Verification times [s]:

(T=2ms)	210	262	16679	2973	459	2

(on a 64 bit Sun Fire X2200 M2 with 8GB RAM)

# Results

Average energy consumption for execution of 10 random traces [mJ]:



- Adaptive DVS is not much worse than OPT (10% on average)
- Adaptive DVS performs better than Static DVS (22% on average)

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## Conclusions

- New adaptive scheme for DVS scheduling of arbitrary non-deterministic event streams bounded by arrival curves
- Combines advantages of offline and online DVS scheduling
- Verification of state-based scheme by means of timed model checking
- Extension to multiple event streams simple but computationally expensive (state space explosion expected)
- Extension to discrete speeds simple (reduced complexity)
- Method not bound to particular power model (only monotonicity and convexity of energy consumption required)
- Open issue: How to best choose the speed of the pessimistic mode (s<sub>max</sub> is not necessarily the best option)

# Thank you!

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